

# Patent Abstracts of Japan

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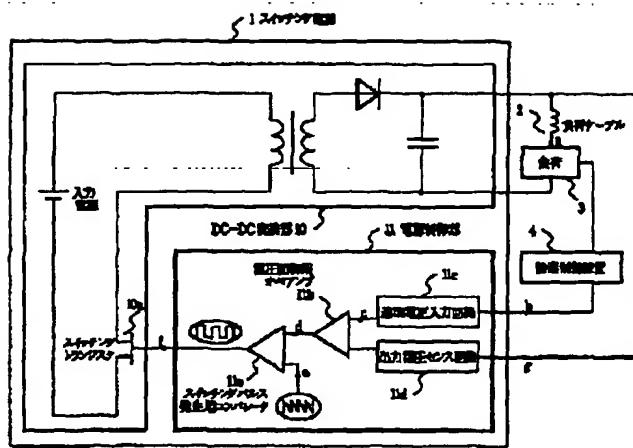
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TITLE : SYSTEM FOR CONTROLLING  
STABILIZATION OF OUTPUT  
VOLTAGE



**ABSTRACT :** PROBLEM TO BE SOLVED: To stabilize the output voltage of a switching power source so that no overvoltage can be applied across an element which is connected to the power source as a load by detecting in advance the abrupt reduction of the load current of the power source and utilizing the detected signal of the abrupt reduction for controlling the width of a switching pulse.

SOLUTION: A logic controller 4 judges the operating state of a load 3 in accordance with a logic control program and, when the controller 4 detects the abrupt reduction of the current consumption of the load 3 in advance, a power source control section 11 drops the reference voltage of a reference voltage input circuit 11c which is the input of an operational amplifier 11b for controlling voltage, and then, reduces the width of the output pulse of a comparator 11a for generating switching pulse. Therefore, the output voltage (g) of a switching power source 1 is dropped by outputting the output pulse of the comparator 11a to the switching transistor 10a of the DC-DC converting section 10 of the power source 1 and, thereafter, the current consumption of the load 3 is abruptly reduced.

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